

19th July, 2019

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING
BOS MEETING

The Agenda to be discussed

1. Electrical related Subjects in I Year.
2. Course Structure of EEE department
3. Course Structure of PE & ED (M.Tech)
4. Evaluation process of UG & PG.
5. Any other matter with permission of Chairman

LIST OF MEMBERS ATTENDED

| S.No. | Name of the Faculty | Designation | Responsibility | Signature |
|-------|-------------------------|---|----------------|-----------------------------|
| 1 | Dr.A.V.Naresh Babu | Professor & Head Dept. of EEE | Chairman | <i>A.V. Naresh Babu</i> |
| 2 | Dr. K.Ravindra | Associate Professor, Dept. of EEE, JNTUK, Kakinada | Member | <i>K.Ravindra</i> |
| 3 | Dr. M.Balasubba Reddy | Associate Professor, Dept. of EEE, CBIT, Hyderabad | Member | <i>M.Balasubba Reddy</i> |
| 4 | Dr. G.V.K. Murthy | Professor of EEE, Dean of Students Affaires, PACE, Ongole | Member | <i>G.V.K. Murthy</i> |
| 5 | Smt.S. Bhargava Lakshmi | Assistant Executive Engineer VTPS, Vijayawada | Member | <i>S. Bhargava Lakshmi</i> |
| 6 | Dr.S.Ravindra | Professor, Dept. of EEE | Member | <i>S.Ravindra</i> |
| 7 | Dr.Ch.Venkata Suresh | Professor, Dept. of EEE | Member | <i>Ch.Venkata Suresh</i> |
| 8 | Dr. D. Srilatha | Associate Professor, Dept. of EEE | Member | <i>D. Srilatha</i> |
| 9 | Mr. Sk.Rasululla | Associate Professor, Dept. of EEE | Member | <i>Sk.Rasululla</i> |
| 10 | Mr. Ch.Rambabu | Associate Professor, Dept. of EEE | Member | <i>Ch.Rambabu</i> |
| 11 | Mr. I. Sai Ram | Work Inspector ANU, Guntur | Member | <i>I. Sai Ram</i> |
| 12 | Dr. Giri Babu Kande | Professor & Head Dept. of ECE | Member | <i>Giri Babu Kande</i> |
| 13 | Dr. R. Eswaraiah | Professor & Head Dept. of CSE | Member | <i>R. Eswaraiah</i> |
| 14 | Dr. M. V. Raghu Ram | Professor & Head Dept. of S&H | Member | <i>M. V. Raghu Ram</i> |
| 15 | Sri T. Srinivasa Rao | Professor & Head T & P | Member | <i>Sri T. Srinivasa Rao</i> |

Minutes of Meeting:

The Department of Electrical and Electronics Engineering 1st Board of Studies Meeting held on 19.07.2019, 9.30 am at department seminar hall.

At the onset the Chairman of BOS, welcomed all the members and introduced internal BOS members to external BOS members. The meeting began with presentation of proposed curriculum by the chair. Further, the agenda has been circulated among the members and discussed, the corresponding resolutions made accordingly.

Item-1: Both AICTE recommended and VVIT proposed curriculums were presented before the BOS members for verification.

Resolution-1: BOS members including Chairman verified both curriculums.

Item-2: VVIT proposed curriculum for 1st B.Tech is presented to verify the list of subjects in semesters 1 & 2.

Resolution-2: BOS members have decided to keep semester subjects same and semester-2 subjects altered in away to include basic subjects related to Electrical & Electronics discipline.

Item-3: Details of Labs in all semester presented before BOS team.

Resolution-3: BOS team decided to conduct Labs after the completion of theory subjects to get more knowledge & effective practical applications.

Item-4: Details of syllabus for the proposed subjects for 1st B.Tech (A.Y 2019-20) is presented before BOS team.

Resolution-4: BOS team is discussed on syllabus of 1st B.Tech subjects and decided to make changes slightly as per the GATE orientation.

Item-5: Details of proposed EEE course structure for UG is presented for discussion.

Resolution-5: BOS team is allotted credits for all subjects and practical Labs as per the weightage of core and Non-core subjects.

Item-6: List of MOOC courses and professional, and open electives are presented before BOS members to discuss importance and impact of electives on students.

Resolution-6: Professional, open electives and MOOC courses are verified. List of electives were changed according to the research applications, also to develop the programming skills to the students by introducing computers related subjects.


Item-7: Details of PG course structure is presented before BOS member.

Resolution-7: BOS members discussed regarding PG course structure, syllabus decided to maintain same.

Item-8: UG course evaluation process is presented before BOS members.

Resolution-8: BOS members discussed about the evaluation process and forwarded to academic council.

The meeting was concluded with the vote of thanks by the BOS Chairman.


(Dr. A. V. Naresh Babu)
Head of the Department
BOS Chairman, EEE
Electrical & Electronics Engineering
Vasireddy Venkatadri Institute of Technology
NANDUR (V), Pedakakani (Mdl.)